FIG.1

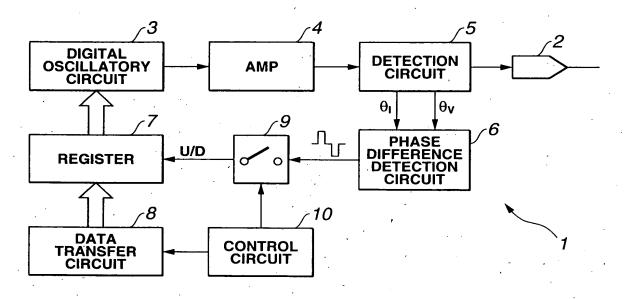


FIG.2

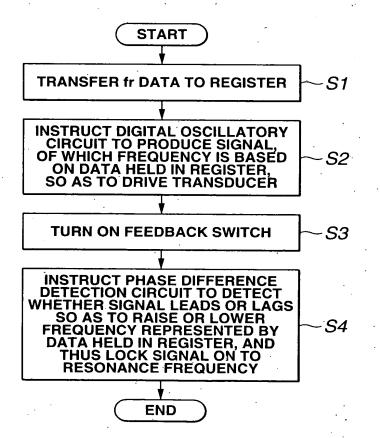


FIG.3

2a

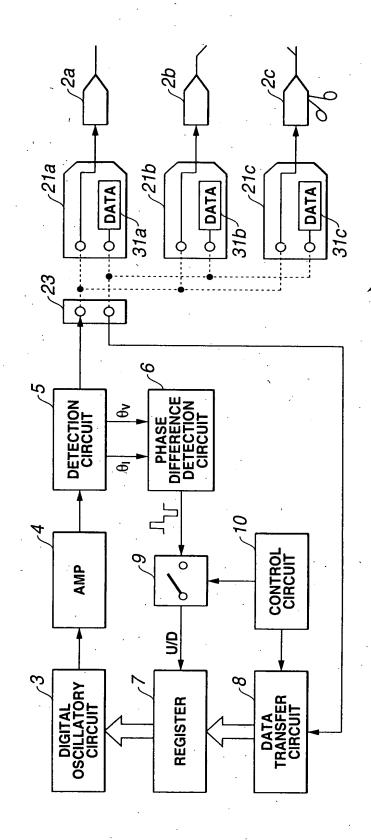


FIG. 4

RESONANCE FREQUENCY DETECTION CIRCUIT DETECTION CIRCUIT 52 Ź PHASE DIFFERENCE DETECTION CIRCUIT PULSE GENERATOR δ. Θ (a)/n ó -55 53 CONTROL 0 AMP Q/O 54 DIGITAL OSCILLATORY CIRCUIT DATA TRANSFER CIRCUIT REGISTER

FIG.5

FIG.6

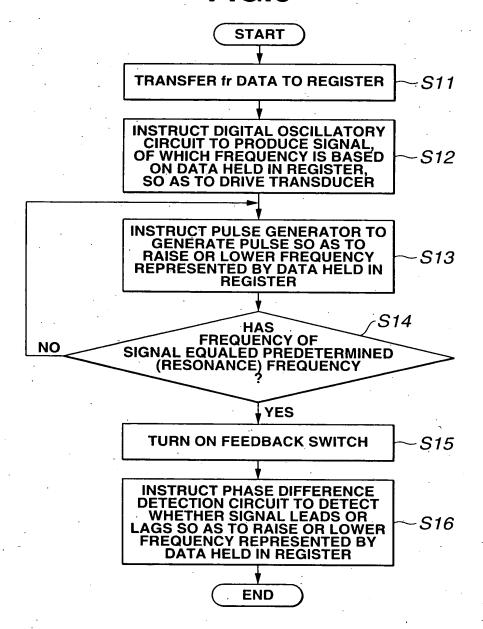
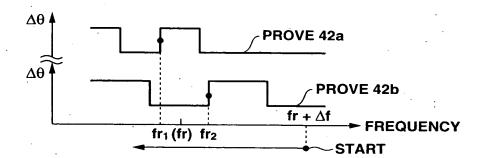


FIG.7



64 FOOTSWITCH -216 -21c ₽ ₽ ₽ ģ δ ф 29 63 OPERATOR PANEL INDICATOR RESONANCE FREQUENCY DETECTION CIRCUIT 51 RECOGNITION CIRCUIT S DETECTION CIRCUIT θ ₽ .52 PHASE DIFFERENCE DETECTION CIRCUIT PULSE GENERATOR 24. D/A Ó CONTROL AMP /8/ CONTROL CIRCUIT (a)/n -53 33 Ō TO D/A Q, ω ά DIGITAL OSCILLATORY CIRCUIT DATA TRANSFER CIRCUIT REGISTER 79 COMPARISON SUB-REGISTER 99

FIG.8

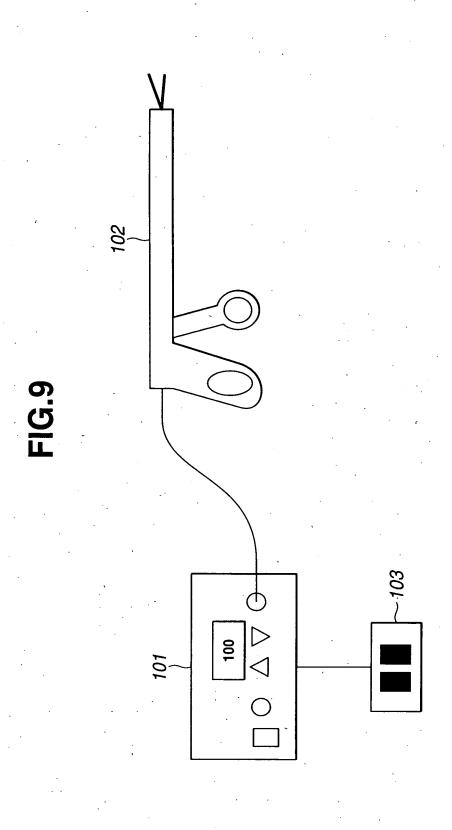
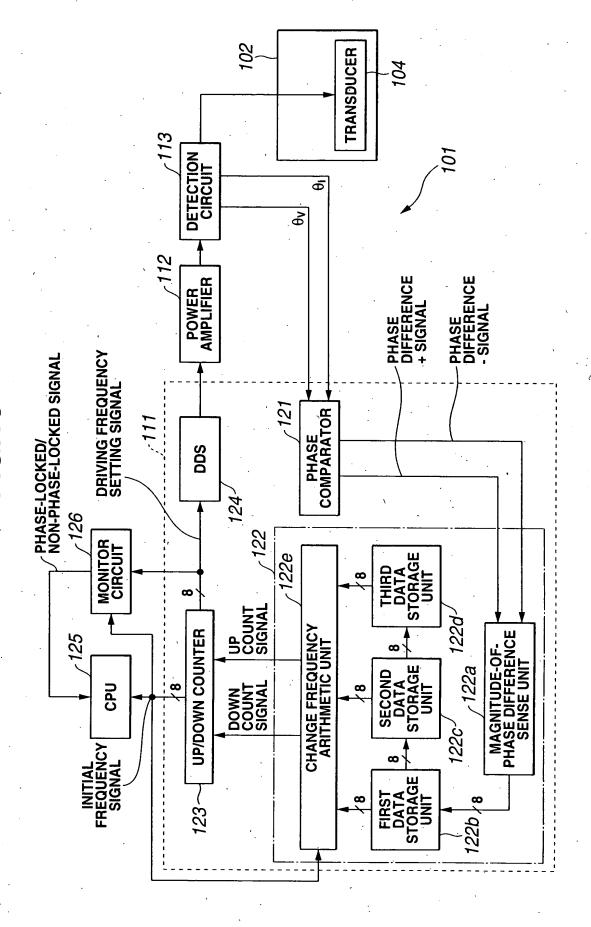
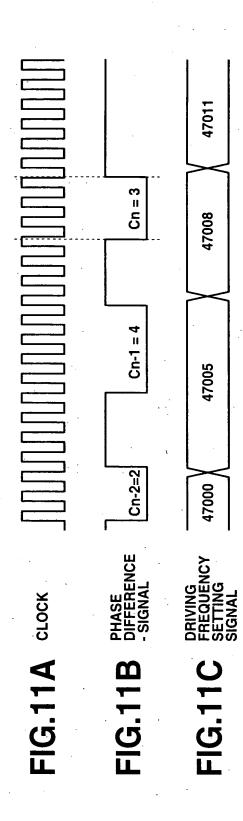


FIG. 10





(Cn-2+Cn-1+Cn)/3 = 3

FIG. 12

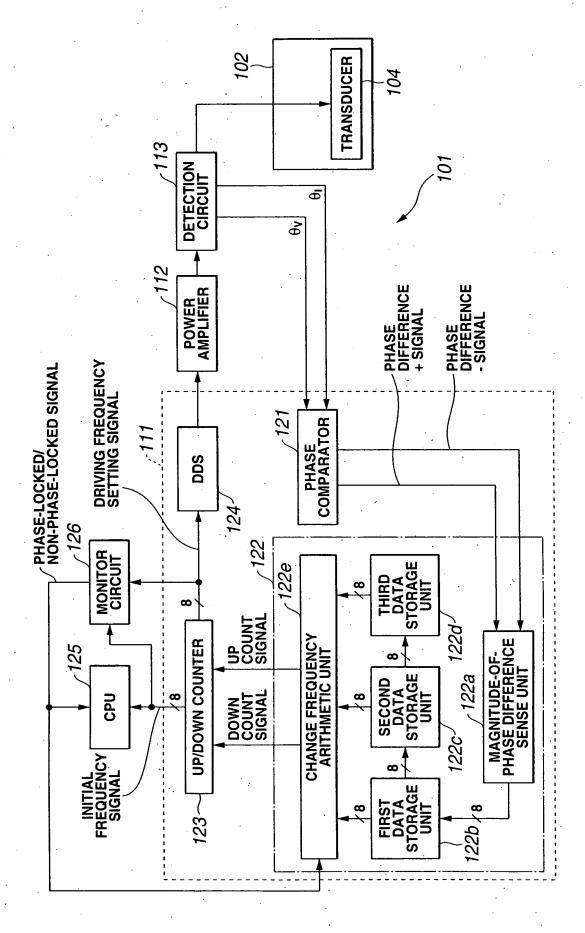


FIG.13

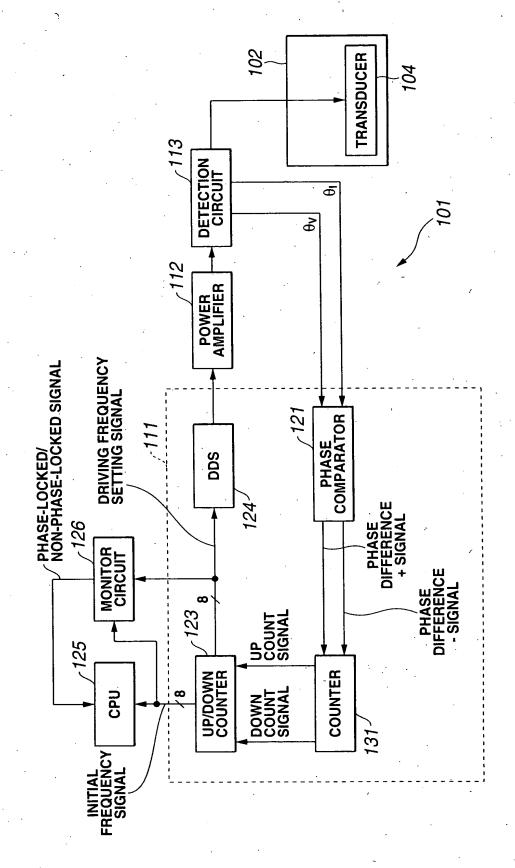
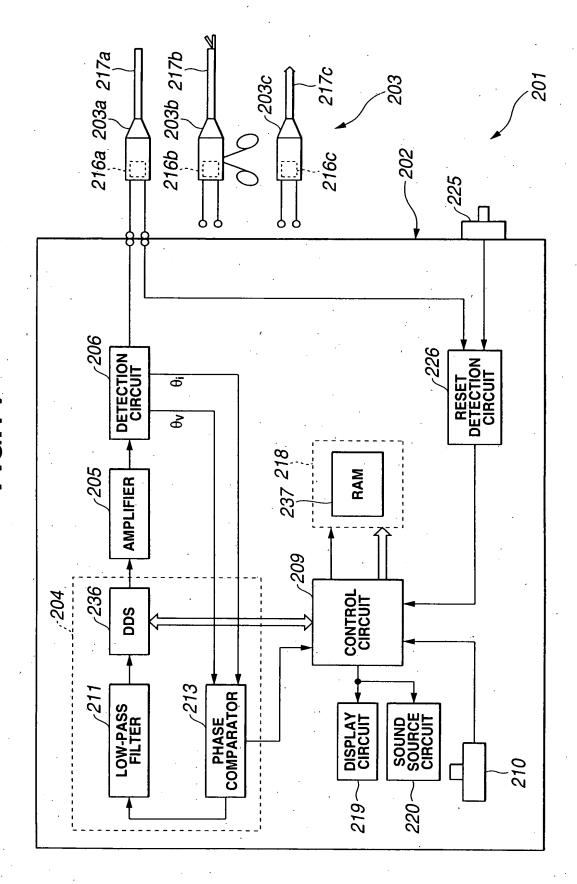


FIG.14



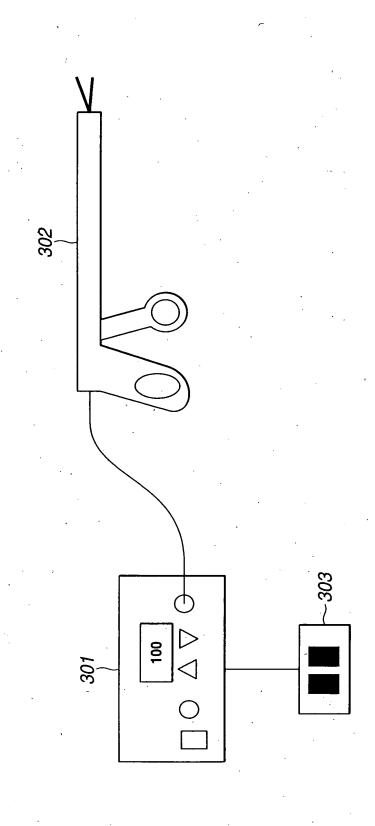
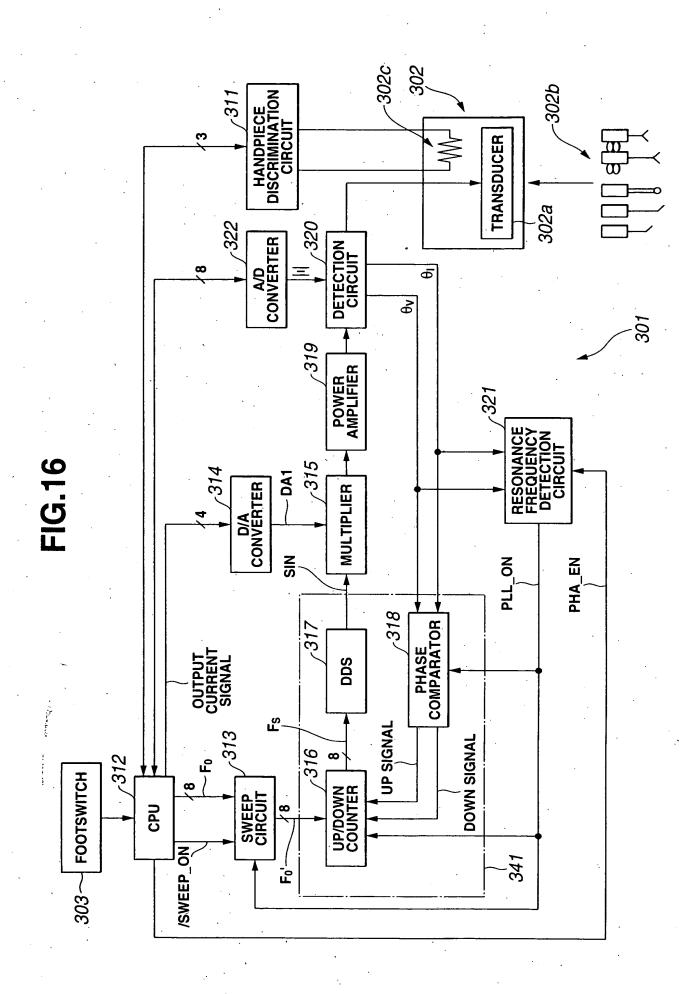


FIG.15



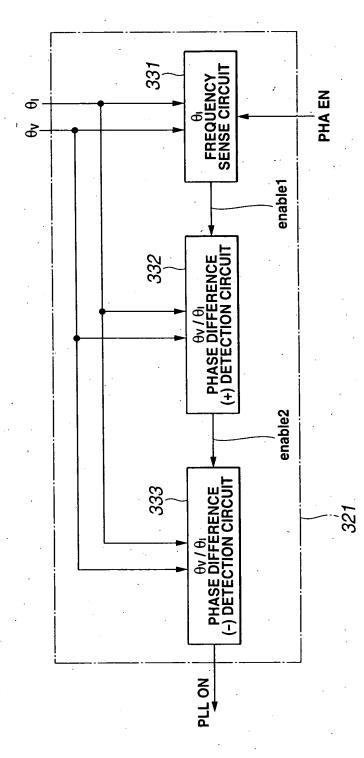


FIG. 17

FIG.18

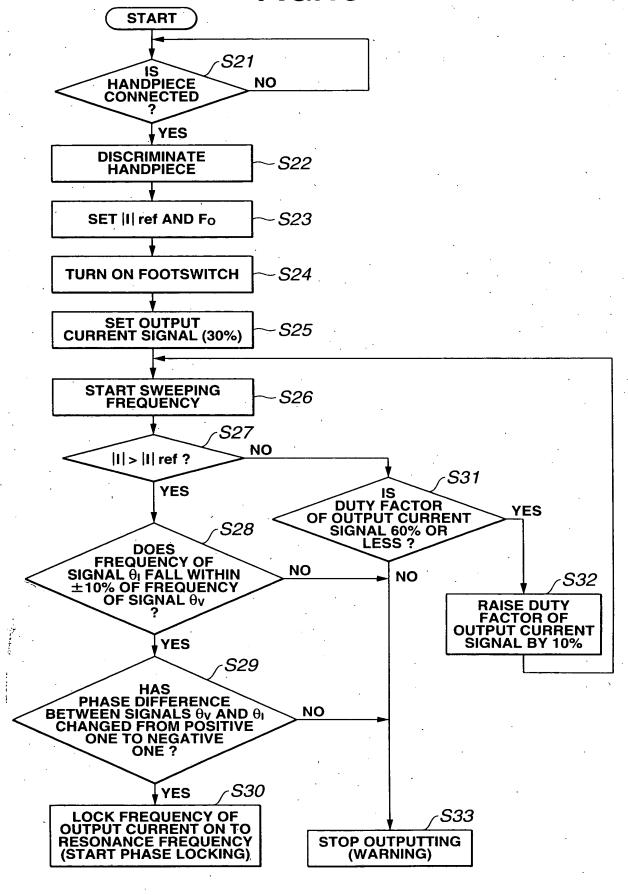


FIG.19

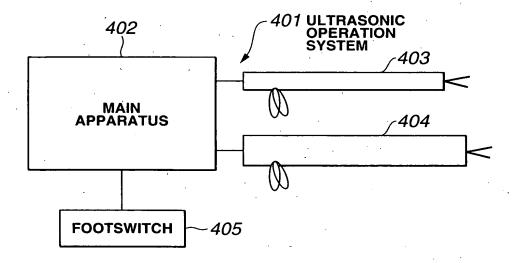


FIG.20

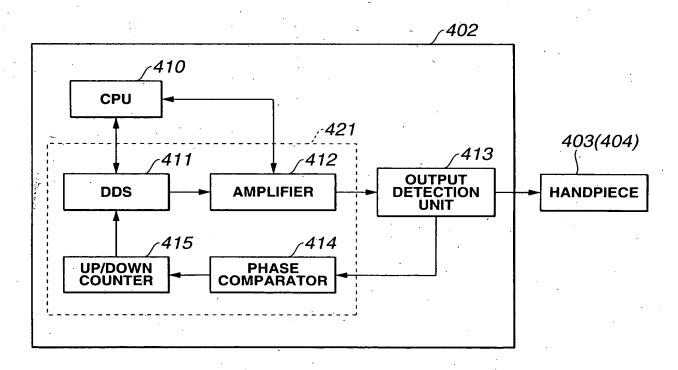


FIG.21

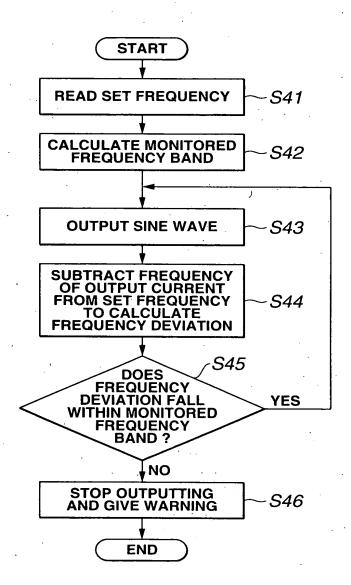


FIG.22

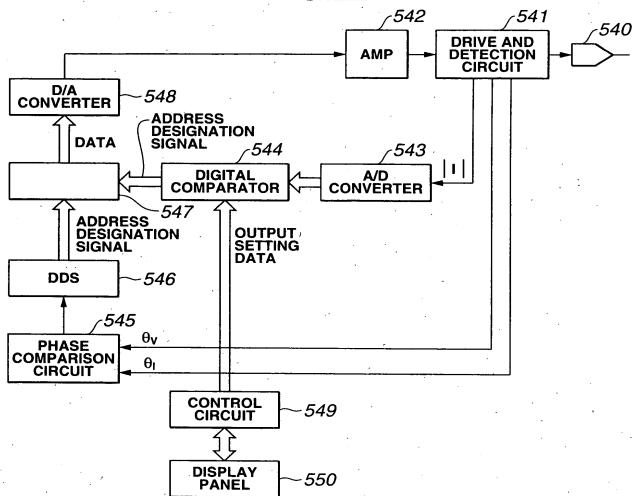


FIG.23

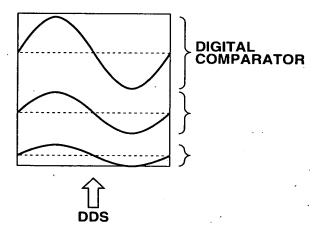


FIG.24 PRIOR ART

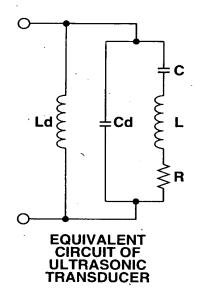


FIG.25 PRIOR ART

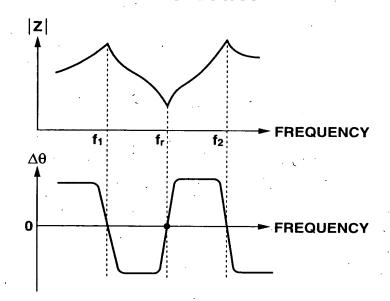


FIG.26 PRIOR ART

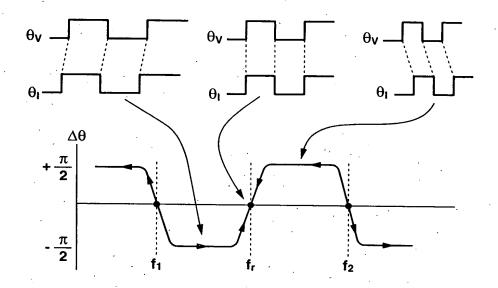


FIG.27
PRIOR ART

